AC (Web) - Bug #2972

Bugs in data being pushed to the UI from the google form - bandaruaakhil@gmail.com

06/22/2024 02:39 AM - jayanthy ramakrishnan

Status: New Start date: 06/22/2024 **Priority: Immediate** Due date: Assignee: Yogesh Sivakumar % Done: 0% Category: **Estimated time:** 0.00 hour Target version: Spent time: 0.00 hour factor: Description Student has said i have backlogs, UI has captured it as no What are the subjects that you liked? ne reasons Do you have any backlogs (arrears) If you answered "Yes" Do you have any If you answered to the above question please specify the names of all the subjects that you had backlogs gap/break in your education/career? to the above que please specify th number of gap you and the list of activities you we involved in durin 6/22/2024 0:16:02 bandaruaakhil@gmail.com I picked this VLSI Design, Digital Logi 10 point scale I had backlogs and I have cleared them all now No, I don't have ANY break alvanize Home > Admission Counselling > All Students > Akhil - bandaruaakhil@gmail.com - Electronics Engineering - Germany/Australia Academics Edit **Profile** Current/Highest **Basic Information** Target Year of Graduation **Previous Applications** B.E / B.Tech 2025 Test Scores • Name of the Institute Affiliated to University Academics Gayatri Vidya Parishad College of Engineering (Autonomous) Jawaharlal Nehru Technological University, Kakin **Publications** Research / Internship Experience Branch/Major CGPA Work Experience Electronics & Communication / Telecommunication Engineering 7.56 / 10 Others Reason for picking this major **University Preferences** The reason I picked this major is because I have a fond interest in it. Essays Documents Subjects you liked Shortlisting VLSI Design, Digital Logic Design. **Applications** Do you have any backlogs/arrears? Financing No, I don't have backlogs Visa

Files

clipboard-202406220809-3ehev.png	48.4 KB	06/22/2024	jayanthy ramakrishnan
clipboard-202406220809-1teqh.png	92.6 KB	06/22/2024	jayanthy ramakrishnan

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